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CS 303 Logic Design - Laboratory Manual 2 LAB 1. LOGIC GATES Objective To get acquainted with the Analog/Digital Training System. To get acquainted with different standard integrated circuits (ICs). To study the basic logic gates: AND, OR, INVERT, NAND, NOR, and XOR. To understand formulation of Boolean function and truth table for logic circuits.

~~CS 303 Logic Design - Laboratory Manual~~

Logic Design Laboratory Manual 2 \_\_\_\_\_ integrated circuit chips available. TTL ICs are usually distinguished by numerical designation as the 5400 and 7400 series. PROCEDURE: 1. Check the components for their working. 2. Insert the appropriate IC into the IC base. 3.

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